Report on Workshop on “Prototype Design and Validation”

Date: 17 August 2026
Venue: Government Science College, Veraval
Event: Workshop on “Prototype Design and Validation”
Name of co Ordinator: Dr. Priya Mange
Name of principal: Dr. Smita Chhag
Organized by: Innovation Cell and SSIP Cell, Government Science College, Veraval
Total no of students:19. M:12. F:7

The Innovation Cell and SSIP Cell of Government Science College, Veraval, in collaboration, successfully organized a Workshop on “Prototype Design and Validation” on 17 August 2026. The workshop was organized under the guidance of Dr. Smita Chhag, Principal, and coordinated by Dr. Priya Mange, Innovation Cell and SSIP Cell Coordinator, with the objective of strengthening students’ practical understanding of prototype development and validation of innovative ideas and Proof of Concepts (PoCs).

The workshop was enriched by the valuable guidance of three distinguished experts: Dr. Vinay Vese, CMFRI; Dr. Mrunal Ambasana, BKNMU; and Mr. Shailesh Suyani, Sagar Manthan Company. Their expert interaction provided students with practical exposure to the process of transforming innovative concepts into workable prototypes and developing solutions that can be tested and improved.

The experts provided practical insights into prototype design and development, with emphasis on planning, building and evaluating a prototype according to the requirements of an innovative idea. Students were guided to understand how a concept can be converted into a functional model and how the development process can be approached systematically.

The workshop also focused on testing and validation of prototypes. Students learned about the importance of evaluating a developed prototype, identifying limitations and areas for improvement, refining the design based on observations and feedback, and progressing towards practical implementation. The sessions connected these activities with the development and validation of Proof of Concepts (PoCs).

The interaction with the experts encouraged students to think practically about their innovative ideas and understand the importance of iterative development, testing, validation and refinement. The guidance helped students gain greater confidence in taking their ideas beyond the conceptual stage and considering their practical implementation.

The workshop successfully contributed to fostering innovation and start-up oriented thinking among students. The organizers sincerely thanked Dr. Vinay Vese, Dr. Mrunal Ambasana and Mr. Shailesh Suyani for their valuable time, guidance and encouragement. The programme provided students with meaningful practical direction for prototype development and strengthened their understanding of the innovation and PoC development process.

Photo 1: Students participating in the workshop session.

Photo 2: Expert interaction and guidance during the workshop.

Photo 3: Experts addressing students on prototype design and validation.

Photo 4: Students and experts during the workshop.

GOVERNMENT SCIENCE COLLEGE, VERAVAL

Name of Event: Workshop on "Prototype Design and validation
Name of Coordinators: Dr. Priya Mange
Total No. of participants: 19 M: 12 F: 7

Attendance:

Sr. No.	Name of participants	Sign.
1)	Solanki Raj D.	Raj
2)	Chudasama Paresh R.	C.P.R
3)	Solanki mandip H.	Mandip
4)	Nandaniya Dhruv R.	
5)	Ludva anirudh P.	
6)	Solanki Dev R.	
7)	Jadav Ashish V.	
8)	Rathod Siddharth M.	
9)	Jamod Meet M.	Meet
10)	Solanki Navneet R.	
11)	Vadhiya Kaushik N.	
12)	Vadhiya Piyush B.	
(12)	Barad Vinayak R.	
13	Gohil Pratiksha M.	P.M. Gohil
14	Chavrecha Pragna A	
15	Gohel Pragna M	
16	Solanki Siddhi D	
17	Zankat Dhara D.	
18	Barad Helasvi M	B.H.M
19.	Vaja Nandita H.	