Vishwakarma Government Engineering College, Chandkheda

Internal Hackathon Event Report for SIH 2026

Event Date & Time: 11th September 2026, 11:00 AM – 4:30 PM

Venue: Vishwakarma Government Engineering College (VGEC), Chandkheda, Ahmedabad, Gujarat

About Smart India Hackathon (SIH)

The Smart India Hackathon (SIH) is a nationwide initiative by the Ministry of Education's Innovation Cell (MIC), AICTE, and Inter-Ministerial organizations. It provides a platform for students to solve real-life challenges faced by ministries, industries, and society. Since its inception in 2017, SIH has grown into the largest open innovation model in the world, engaging in lack of students every year.

VGEC Event Highlights – 11th Sept 2026

Total Teams Participated	121
• Software Teams	77
• Hardware Teams	44
Format	10 minutes per team (7 min presentation + 3 min Q&A)
Jury Panel	External experts (Industry person, Academic) and Faculty members
Event Duration	11:00 AM – 4:30 PM

Significance of the Event

This nodal center round was not just an internal screening, but a recognized evaluation stage under SIH 2026. VGEC hosted teams from across Gujarat and nearby regions, creating a vibrant atmosphere of competition, collaboration, and innovation. The event served as a critical filter for shortlisting teams that will progress toward the national grand finale of SIH.

It offered a platform for students to validate prototypes with expert feedback, experience real hackathon presentation under time constraints, and network with peers, mentors, and evaluators.

Key Takeaways

- Scale of Participation: The large number of teams (121) highlighted enthusiasm for innovation.
- Exposure: Students practiced pitching solutions within strict timelines.
- Encouragement of Innovation: Prototypes addressed diverse problem statements in agriculture, healthcare, sustainability, education, smart mobility, and public services.
- Impact: Prepared students to represent their institutes nationally, positioning VGEC as a hub of innovation.

Judging Process:

The teams participating in the Internal Hackathon at VGEC, Chandkheda, were evaluated through a structured, systematic, and transparent judging process. Separate and clearly defined evaluation criteria were established for the hardware and software categories to ensure an appropriate assessment of each project. Each team was evaluated on a total score of 100 marks, thereby maintaining consistency, fairness, and uniformity throughout the evaluation process.

Judging Criteria:

A. Software Project Evaluation Rubric – 100 Marks (For Internal Hackathon 2026)

1) Problem Identification & Understanding (15 Marks)

2) Innovation & Creativity (20 Marks)

3) Technical Design & Implementation (25 Marks)

4) Working Prototype & Demonstration (20 Marks)

5) Impact, Feasibility & Scalability (10 Marks)

6) Presentation & Q&A (10 Marks)

B. Hardware Evaluation Rubric – 100 Marks (For Internal Hackathon 2026)

1) Problem Identification & Engineering Relevance (15 Marks)

2) Innovation & Engineering Creativity (20 Marks)

3) Hardware Design & Technical Implementation (25 Marks)

4) Working Prototype & Performance (20 Marks)

5) Feasibility, Cost & Scalability (10 Marks)

6) Presentation & Q&A (10 Marks)

Based on this evaluation framework, the best-performing teams from both hardware and software categories were shortlisted for nomination to SIH 2026, ensuring that only the most innovative and impactful solutions progressed further.

Jury Panel Details:

The jury panel is selected as per the expertise of subject and they were allocated group of student's teams as per the problem statement of SIH 2026. The Jury panel details is as below:

1. Ms. Kanan Yagnik
2. Dr. Swati Patel
3. Mr. Suprit Jayntilal Patel
4. Dr. Alpesh H. Dafda
5. Mr. Jignesh Borisagar
6. Mr. Abhi Shah
7. Dr. Vishal Barot
8. Mr. Trushal Prajapati
9. Dr. Ashish Unnarkat
10. Dr. Chirag N Patel
11. Dr. Hetal A. Joshiyara
12. Dr. Pankajkumar P. Prajapati
13. Prof. Purvi Ramanuj
14. Dr. Kinjal Ravi Sheth
15. Dr. Viral Borisagar
16. Dr. Shahid S.Modasiya
17. Prof. Khushali Raval
18. Dr. Nitinkumar J. Bathani
19. Dr. Kaustubh Vyas
20. Dr. Balvant J. Makwana
21. Prof. Anita Solanki
22. Dr. Jalpa Ramawat
23. Dr. M. M. Sharma
24. Prof. H. S. Wani
25. Dr. N S Mehta
26. Dr. Pinal J. Patel
27. Prof. A.N. Vaghela
28. Prof. M. D. Khediya
29. Dr. P K Shah
30. Dr. K. R. Patel

Judges' information:

Ms. Kanan Yagnik Senior Analyst, Accenture Ahmedabad, Gujarat, India
Dr. Swati Patel Assistant Professor Department of Information Technology L.D. College of Engineering (LDCE) Navrangpura, Ahmedabad, Gujarat, India
Mr. Suprit J. Patel Founder, iNEXUS Technology Ahmedabad, Gujarat, India
Dr. Alpesh H. Dafda Associate Professor and Head Department of Electronics & Communication Engineering Government Engineering College, Modasa Dist. Aravalli, Gujarat, India
Mr. Jignesh H. Borisagar Executive Engineer (R&D) (I/c) Gujarat Power Research & Development (GPRD) Cell GUVNL IITGn Research Park, IIT Gandhinagar, Palaj, Gandhinagar, Gujarat, India
Mr. Trushal Prajapati Founder & Managing Director HJ Virtual Engineering Services (HJVES), Ahmedabad, Gujarat, India
Dr. Ashish Unnarkat Assistant Professor & Head Department of Chemical Engineering, School of Energy Technology (SoET) Pandit Deendayal Energy University (PDEU), Raisan, Gandhinagar, Gujarat, India
Mr. Abhi Shah, Junior Software Architect Distinction Dev 202, 3rd Eye II, Opp. Parimal Garden, Panchavati Road Chimanlal Girdharlal Road, Ahmedabad, Gujarat, India
Dr. Vishal Barot, Assistant Professor & Head Department of Computer Engineering GTU Institute of Technology & Research (GTU-ITR) Mehsana, Gujarat, India
Dr. Chirag N Patel Associate Professor Government Engineering College, Gandhinagar Sector 28, Gandhinagar–Vijapur Road Gandhinagar, Gujarat, India

Total participating team & students:

The Smart India Hackathon at Vishwakarma Government Engineering College (VGEC), Chandkheda, scheduled for 11th September 2026, reflects the large-scale participation of students across different domains of innovation. The final schedule categorizes teams into software and hardware tracks to ensure clarity and effective organization. A total of **121 teams** have been registered to take part in this event. Out of these, **77 teams** are dedicated to **software**-related problem statements, while **44 teams** are working on **hardware**-based problem statements. This distinction highlights the diverse nature of challenges being addressed during the hackathon.

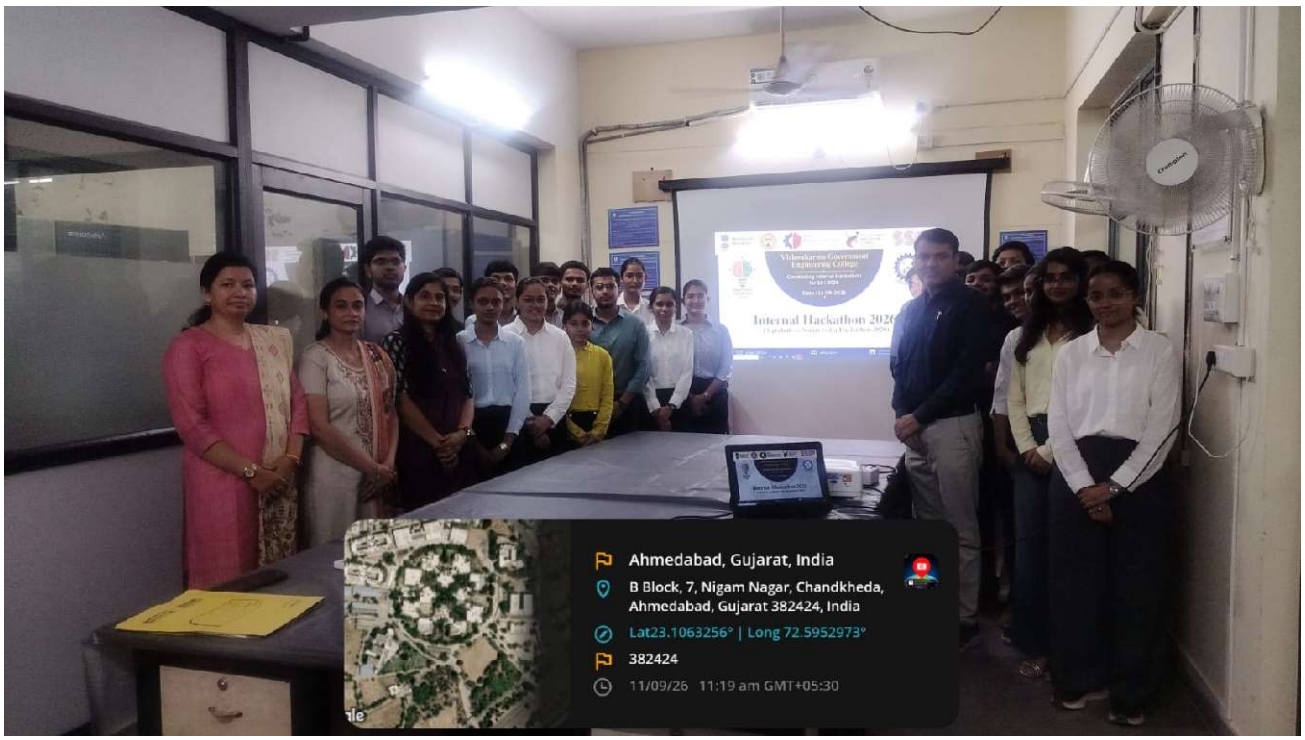
Each team has six members, which significantly increases the scale of student involvement. In the software category, 77 teams collectively bring together 462 students, all working on creative programming, digital solutions, and software-driven problem-solving. On the other hand, the hardware category consists of 44 teams, representing 264 students, who focus on tangible models, devices, and engineering-based solutions. Combined, this accounts for a total of **726 students** actively participating in the hackathon.

This distribution of teams not only underlines the balance between software and hardware innovation but also emphasizes the college's commitment to encouraging multidisciplinary participation. The event serves as a platform where hundreds of students collaborate, compete, and showcase their technical creativity.

Social media promotion (On VGEC official Instagram page):

#sih2026 #smartindiahackathon2026

Event photos:



Participating teams' pictures: